
“Design of Reliable Electronic Products”

Friday, May 25th, 2012

Dr. Michael H. Azarian

Center for Advanced Life Cycle Engineering (CALCE)

A. James Clark School of Engineering - University of Maryland (USA)

and

Prof. Flavio G. Canavero

Dipartimento di Elettronica e Telecomunicazioni – Politecnico di Torino

Course agenda

Time	Topic
9:00-9:15	Registration and Coffee
9:15-9:30	Introductions of instructors and participants
9:30-10:00	<i>Introduction to Design for Reliability (DfR)</i>
10:00-10:45	<i>DfR Steps 1-3: Product Requirements</i>
10:45-11:00	Break
11:00-11:30	<i>Physics of Failure Concepts</i>
11:30-12 noon	<i>DfR Steps 4-5: Part Selection and Analysis</i>
12 noon – 13:00	Lunch
13:00-13:15	<i>DfR Step 6: Physics of Failure Virtual Qualification</i>
13:15-14:30	<i>DfR Steps 7-8: Prototype Evaluation</i>
14:30-15:00	<i>EMI/EMC and Signal Integrity Impact on Reliability</i>
15:00-15:15	Break
15:15-16:00	<i>EMI/EMC and Signal Integrity Impact on Reliability (continued)</i>
16:00-16:15	<i>DfR Step 9: Process Qualification</i>
16:15-17:00	<i>DfR Steps 10-12: Closed Loop Reliability Management</i>
17:00	Close